

Seva sadan Mahavidhyalaya Burhanpur (M.P.)
Daily- Diary

2023 - 2024

Prof. Narendra Bhardwaj

Computer. Department.

SEVA SADAN MAHAVIDYALAYA, BURHANPUR (M.P.)

Subject Computer System

Architecture

PROFESSOR'S DAILY DIARY

Class BSC Ist yr

Date	Unit	Period	Matter + Teaching Method	Assignment
7/8	I	I	Introduction of student	
8/8	-	I	Introduction to fundamentals	
9/8	-	I	Introduction to electronics	
10/8	-	I	Introduction to comp. organization	
11/8	-	I	Introduction to Comp. Architecture	
12/8	-	I	Data types Introduction	
14/8	-	I	Types of data types.	
16/8	-	I	complements 1's & 2's	
17/8	-	I	9's & 10's complement	
18/8	-	I	fixed point	
19/8	-	I	floating point	
21/8	-	I	Number systems	
22/8	-	I	Types of no system	
23/8	-	I	Binary	
24/8	-	I	octal	
25/8	-	I	decimal	
26/8	-	I	Hexa decimal.	
31/8	I	I	error detection	
1/9	I	I	excess 3 code.	
2/9	I	I	Boolean expression Intro.	
4/9	I	I	laws of Boolean Algebra	
5/9	I	I	commutative law.	
6/9	I	I	Associative distributive laws	
8/9	I	I	distributive laws	
11/9	I	I	De Morgan's Laws	
12/9	I	I	combinational ccts.	
13/9	I	I	multiplexor	
15/9	I	I	logic diagram of multiplexor	
18/9	I	I	multiplexor Block diagram	

SEVA SADAN MAHAVIDYALAYA, BURHANPUR (M.P.)

PROFESSOR'S DAILY DIARY

Class 85C 2nd year

Subject Computer System

PROFESSOR'S DAILY DIARY

Class 85C 2nd year

Date	Unit	Period	Matter + Teaching Method	Assignment
20/9/23	I	I	Multiple choice continued	
22/9/23	I	I	Demultiplexer	30/11/23
23/9/23	I	I	Count down	1/12/23
25/9/23	I	I	Types of counter	2/12/23
26/9/23	I	I	Combinational circuit	7/12/23
3/10/23	I	I	Sequential circuit	8/12/23
4/10/23	I	I	Octal decoder	14/12/23
5/10/23	I	I	'Adder'	15/12/23
6/10/23	I	I	Clear test of unit 1 st	18/12/23
7/10/23	I	I	Subtractor	19/12/23
9/10/23	I	I	Half adder	20/12/23
10/10/23	I	I	Full adder	8/12/23
11/10/23	I	I	Encoder	10/12/23
12/10/23	I	I	Decoder	16/12/24
16/10/23	I	I	Registers, flip flops	17/12/24
17/10/23	I	I	Counter	30/1/24
26/10/23	I	I	Counters continued	8/2/24
30/10/23	I	I	Instruction codes	9/4/24
31/10/23	I	I	Instruction codes continued...	12/2/24
4/11/23	I	I	Operation format	13/2/24
7/11/23	I	I	Direct & Indirect Addressing	14/2/24
8/11/23	I	I	Offset addressing	15/2/24
30/11/23	I	I	Computer Registers	19/2/24
21/11/23	I	I	Computer instructions	12
22/11/23	I	I	Timing & control	23/1/24
23/11/23	I	I	Timing & control continued	
25/11/23	I	I		
28/11/23	I	I	Instruction cycle	
29/11/23	I	I	Fetch cycle	

PRINCIPAL

Seva Sadan Mv.

Burhanpur (M.P.)

SEVA SADAN MAHAVIDYALAYA, BURHANPUR (M.P.)

Subject Cyber Security

PROFESSOR'S DAILY DIARY

Class BCA III yr

Date	Unit	Period	Matter + Teaching Method	Assignment
21/8	I	III	Introduction to subject	
22/8	I	III	What is cyber security	
23/8	I	III	Need for security	
24/8	I	III	Basics of cyber security	
25/8	I	III	Threats to information	
26/8	I	III	Encryption	
31/8	I	III	decryption	
1/9	I	III	Need of Information Security.	
4/9	I.	III	Cryptography Basics	
5/9	I	III	Symmetric key cryptography.	
6/9	I	III	Asymmetric key cryptography	
8/9	I	III	Hash function cryptography	
13/9	I	III	features of Hash functions.	
12/9	I	III	Data integrity check.	
13/9	I	III	Caesar cipher substitution	
15/9	I	III	substitution techniques	
20/9	I	III	Mono alphabetic substitution technique	
22/9	I	III	poly alphabetic substitution technique	
25/9	I	III	Vigenere cipher technique.	
26/9	I	III	Vigenere cipher Example.	
27/9	I	III	Advantages of Vigenere cipher	
3/10	I	III	Transposition cipher	
4/10	I	III	Playfair cipher	
5/10	I	III	problems on transposition	
6/10	I	III	problems on transposition	
9/10	I	III	Class test.	
10/10	II	III	Encryption & decryption	
11/10	II	III	Symmetric key Algorithm	
12/10	II	III	Brief History of asymmetric	

Class BCA III

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PRINCIPAL
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Burlanpur (S. P.)

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SEVA SADAN MAHAVIDYALAYA, BURHANPUR (M.P.)

Subject ^{System Analysis and design} PGDCA Ist sem

PROFESSOR'S DAILY DIARY

Class PGDCA Ist sem

Assignment

Date	Unit	Period	Matter + Teaching Method	Assignment
7/8	I	IV	Introduction	
8	-u	-u	introduction of students	
9	-u	-u	introduction of system	
10	-u	-u	types of system	
11	-u	-u	Classification of system	
12	-u	-u	Characteristics of system	
14	-u	-u	Software models.	
16	-u	-u	Introduction to S/w Company.	
17	-u	-u	Preliminary investigation	
18	-u	-u	feasibility study	
19	-u	-u	Technical feasibility	
21	-u	-u	operational feasibility	
22	-u	-u	financial feasibility	
23	-u	-u	Analysis	
24	-u	-u	Design	
25	-u	-u	Coding	
26	-u	-u	testing	
31	-u	-u	SOLC.	
4/9	-u	-u	waterfall model Basic	
5/9	-u	-u	waterfall model Description	
5/9	-u	-u	prototype model Basic intro	
6/9	-u	-u	prototype Model continued....	
8/9	-u	-u	implementation.	
11/9	-u	-u	post implementation	
12/9	-u	-u	prototype Adv & Dis Adv	
13/9	-u	-u	Spiral model	
20/9	-u	-u	Adv & Dis Adv of spiral model	
22/9	-u	-u	Information Cost & benefit analysis	
25/9	-u	-u	Information finding methods	

SEVA SADAN MAHAVIDYALAYA, BURHANPUR (M.P.)

Subject System Analysis

PROFESSOR'S DAILY DIARY

Class PGDCA I Sem

Date	Unit	Period	Matter + Teaching Method	Assignment
26/9/23	I	IV	Information finding techniques	
27/9/23	I	IV	Interviews	
3/10/23	II	IV	Analysis of Structured Analysis	
4/10/23	II	IV	Tools of Structured Analysis	
5/10/23	II	IV	Flow Chart	
6/10/23	II	IV	DFA Introduction	
9/10/23	II	IV	DFA types	
10/10/23	II	IV	DFA levels	
11/10/23	II	IV	Data Dictionary	
17/10/23	III	IV	System the Input, output	
18/10/23	III	IV	Database table, forms, Database	
20/10/23	III	IV	file design	
26/10/23	III	IV	form design continued	
7/11/23	III	IV	Testing	
20/11/23	III	IV	error, bug & failure	
21/11/23	III	IV	Testing Concepts	
22/11/23	IV	IV	Implementation & post implementation	
25/11/23	IV	IV	post implementation	
29/11/23	IV	IV	Maintainance & extendability	

4-17-23
 Seva Sadan NV.
 Burhanpur (M.P.)

P. SEVA SADAN MAHAVIDYALAYA, BURHANPUR (M.P.)

Subject Computer Org & Architecture PROFESSOR'S DAILY DIARY

Class BCA Ist yr

Date	Unit	Period	Matter + Teaching Method	Assignment
7/8	I	V	Introduction of College	
8	I	V	Introduction to Computer Fundamentals	
9	I	V	Introduction to Electronics	
10	I	V	Introduction to. fundamentals of org	
11	I	V	Introduction to. Architecture	
12	I	V	Definition of Computer	
14	I	V	Characteristics of Computer	
16	I	V	Capabilities of Computer	
17	I	V	Advantages of Computer	
18	I	V	Disadvantages of Computer	
19	I	V	Uses of Computer	
21	I	V	Block Diagram of Computer	
22	I	V	Work Station	
23	I	V	Smart System	
24	I	V	Classification of Smart System	
25	I	V	Uses of Computer in e-governance	
26	I	V	Uses of Computer in public domain	
31	I	V	Cloud Computing	
4/9	I	V	Types of Computer	
4/9	I	V	Analog	
5/9	I	V	Digital	
6/9	I	V	Generations of Computing	
8/9	I	V	Gen. of Computer Continued.....	
9/9	I	V	Types of Computer	
11/9	I	V	Types of Computer Continued....	
20/9	I	V	mini	
22/9	I	V	micro	
23/9	I	V	Mainframe	
25/9	I	V	GPS	

SEVA SADAN MAHAVIDYALAYA, BURHANPUR (M.P.)

PROFESSOR'S DAILY DIARY

Class B.A 1st yr

PROFESSOR'S DAILY DIARY

Class B.A 1st yr

Subject <u>Computer Organization & Architecture</u>				Subject <u>Computer Organization & Architecture</u>					
Date	Unit	Period	Matter + Teaching Method	Assignment	Date	Unit	Period	Matter + Teaching Method	Assignment
26/11	II	V	Block Diagrams of Computer		20/12/23	4	V	Logical, Arithmetic & Shift micro operation	
27/11	II	VI	Functioning of ALU		22/12/23	3	V	Digital electronics introduction	
3/12	II	VI	Binary & octal		27/12/24				
4/12	II	VI	Types of register		10/1/24	3	V	Number system conversion	
5/12	II	VI	Output devices K.B., mouse		6/1/24	3	V	Logic gates	
6/12	II	VI	Secondary & its types		12/1/24	3	V	Code conversion	
9/12	II	VI	Light pen		8/2/24	3	V	Multiplexer & Demultiplexer	
10/12	II	VI	Barcode & omr		9/2/24	3	V	Multiplexer	
11/12	II	VI	OCR meter		14/2/24	3	V	Demultiplexer & Decoder	
12/12	II	VI	Track ball touch screen		22/2/24	3	V	Flip flop	
17/12	II	VI	Optical discs types & mounting		23/2/24	3	V	Counting	
18/12	II	VI	Flat panel LED LCD display		26/2/24	6	V	Introduction to I/O	
19/12	II	VI	Printers & its types impact & non-impact		27/2/24	6	V	Peripherals & I/O interface	
20/12	II	VI	3-D printers, plotted & their types		29/2/24	6	V	Programmed I/O & interrupt I/O	
21/12	II	VI	Instruction codes		4/3/24	6	V	Direct memory access	
21/12	II	VI	Instruction format						
21/12	II	VI	Computer instruction						
21/12	II	VI	Computer systems						
21/12	II	VI	Timing & control						
21/12	II	VI	Instruction cycle						
21/12	II	VI	Instruction cycle - fetch, decode, execute						
21/12	II	VI	Instruction cycle continued						
21/12	II	VI	Addressing mode						
21/12	II	VI	Implicit, immediate, direct, indirect, register indirect						
21/12	II	VI	Machine language & Assembly language						
21/12	II	VI	Assembly level language						
21/12	II	VI	Registers, transfer & arithmetic operation						
21/12	II	VI	Machine operation						
21/12	II	VI	Register transfer language						
21/12	II	VI	Register transfer language						